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Atty. Docket No.: M-15170 US

July 21, 2003

Mail Stop Patent Application Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s):

Kevin E. Sallese

Title:

Programmable Logic Device With A Memory-Based Finite State Machine

X Return Receipt Postcard

This Transmittal Letter (in duplicate)

X Twenty (20) page(s) Specification (not including claims)

X Seven (7) page(s) Claims

X One (1) page Abstract

X Four (4) sheet(s) of Drawings including Figures 1a, 1b, 2, 3 and 4

X Two (2) page(s) Declaration For Patent Application and Power of Attorney

X Two (2) page(s) Assignment and Recordation Form Cover Sheet

X One (1) page Non-Publication Request

**CLAIMS AS FILED** 

| <u>For</u><br>Total Claims            | Number<br><u>Filed</u><br>20 | -20 | =   | Number <u>Extra</u> 0 | X  | <u>Rate</u><br>\$18.00 | = | \$<br>\$ | Basic Fee<br><u>740.00</u><br>0 |
|---------------------------------------|------------------------------|-----|-----|-----------------------|----|------------------------|---|----------|---------------------------------|
| Independent<br>Claims                 | 3                            | -3  | === | 0                     | X  | \$84.00                | = | \$       | 0                               |
| Fee ofmultiple de                     | r more                       |     | ·   |                       | \$ |                        |   |          |                                 |
| Fee for Request for Extension of Time |                              |     |     |                       |    |                        |   | \$       |                                 |
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Please make the following charges to Deposit Account No. 50-2257:

Total fee for filing the patent application in the amount of

\$ 740.00

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2257.

**EXPRESS MAIL LABEL NO:** 

EV252519785US

Respectfully submitted,

Jon W. Hallman

Attorney for Applicants

Reg. No. 42,622

**MODIFIED PTO/SB/35 (11-00)** 

## REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)

| Inventor           |  | Kevin E. Sallese |            |  |  |  |
|--------------------|--|------------------|------------|--|--|--|
| Title              | Programmable Logic Device With A Memory-Based Finite State Machine |                  |            |  |  |  |
| Atty Docket Number |  |                  | M-15170 US |  |  |  |

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

July 21, 2003 Date

Jon W. Hallman Attorney for Applicants Reg. No.: 42,622

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).